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RESEARCH ARTICLE



DESIGN OF FIFTH ORDER CONTINUOUS TIME-DELTA SIGMA ADC USING SIMSIDES

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Abstract— This paper brief the use of very high precision Noise shaping sigma delta modulation techniques for high applications that require a signal-to-noise ratio and high resolution. A continuous-time delta-sigma A/D modulator with OSR of 40, signal bandwidth of 1.5625 MHz and clocked at 500MHz is implemented. This achieves 78 dB SNR and 12 bits of resolution.

Keywords: Over Sampling Ratio, Signal to Noise Ratio, Digital to Analogue Converters, Delta-Sigma Modulator.

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