



RESEARCH ARTICLE

**POWER AND AREA EFFICIENT
DESIGN OF COUNTER FOR LOW
POWER VLSI SYSTEM**

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Abstract— The objective of this project is to design the sequential circuit's using different sequential circuit components and analyze the different characteristics such as power conception and area occupation etc. by sequential circuit designed using microwind.

Full Text: <http://www.ijcsmc.com/docs/papers/June2013/V2I6201367.pdf>